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**REMARKS**

In accordance with the foregoing, and claims 3-4, 7, 12, 16-18, 20, 22, 24, 26, 33 and 38 are amended, and a replacement abstract is provided. No new matter is presented in any of the foregoing and, accordingly, approval and entry of the amended claims and abstract are respectfully requested.

Claims 1-26, 33-38, 40 and 41 are pending and under consideration.

Claims 1, 2, 9-11, 13-15, 19, 21, 23, 25, and 40 are allowed. In item 7 the Examiner indicates that 3-8, 12, 16-18, 20, 22, 33-38 and 41 are allowable if rewritten to overcome the objections of item 3. In item 8, the Examiner indicates that claims 24 and 26 are allowable if rewritten or amended to overcome the rejection of item 5. Applicants appreciate the indication of allowable subject matter.

**ITEM 2: OBJECTION TO THE SPECIFICATION**

In item 2, the Examiner objects to the specification in that a copy of the replacement abstract in the Amendment filed June 26, 2003 was not received.

A copy of the filed Abstract is included in the amendment as a replacement Abstract herein, and withdrawal of the objection to the specification is requested.

**ITEM 3: OBJECTION TO CLAIMS 3-8, 12, 16-18, 20, 22, 33-38 AND 41**

In item 3, the Examiner objects to claims 3-8, 12, 16-18, 20, 22, 33-38 and 41 because of informalities.

Claims 3-4, 7, 12, 16-18, 20, 22, 33, 36, and 38 are amended herein as suggested by the Examiner. Withdrawal of the objection is requested.

**ITEM 5: REJECTION OF CLAIMS 24 AND 26 UNDER 35 U.S.C. §112, SECOND PARAGRAPH**

In item 5, the Examiner rejects claims 24 and 26 under 35 U.S.C. §112, second paragraph as being indefinite.

The Examiner contends claim 24 is indefinite since "a digital-to analog converter" does not have connection or cooperation with the elements recited in claim 23.

The Examiner contends that claim 26, line 2 is indefinite since claim 26 does not clearly indicate "what element(s) is/are included in the second part of said digital circuitry." The Examiner also indicates "the first part of said digital circuitry includes an input signal processing circuit, a first and second clocked elements and a clock generating circuitry." (Action at page 4).

Claim 24 is amended herein to recite "a digital-to-analog converter as a part of the

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analogy circuitry to produce the one or more analog signals(s)." (See, for example, page 12, lines 11-12 that indicate "mixed-signal circuitry maybe (or include) a digital-to-analog converter."

Claim 26 is amended herein to address the Examiner's concern and recite "said second part of said digital circuitry is at least one of said input signal processing circuit, said first clocked element, and said clock generating circuitry."

Applicants submit that claims 24 and 26 comply with 35 U.S.C. §112, second paragraph and request withdrawal of the rejection.

### CONCLUSION

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8(a)  
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450  
On Nov. 28, 2005  
STAAS & HALSEY  
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Date 11/28/05